

## PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor: Robert M. Zeidman

Atty.Dkt.No.: 6257-16302

Serial Number: 10/044,217

Examiner: Luu, Cuong V.

Filing Date: November 19, 2001

Group/Art Unit: 2128

Title: SYSTEM AND METHOD FOR  
CONNECTING A LOGIC  
CIRCUIT SIMULATION TO A  
NETWORK

Conf. No.: 9153

\*\*\*CERTIFICATE OF E-FILEING TRANSMISSION\*\*\*

I hereby certify that this correspondence is being transmitted  
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On: June 7, 2010  
Date

/Alex A. Courtade/  
Alex A. Courtade, #65,635

## **PETITION UNDER 37 C.F.R. §1.136 FOR EXTENSION OF TIME**

Applicant respectfully petitions the Commissioner for a 1 month extension of time under 37 C.F.R. §1.136 within which to respond to the Official Action mailed February 5, 2010, such extension allowing the undersigned until June 7, 2010 (the next business day following a weekend), to respond.

A fee authorization is enclosed to cover this extension fee. However, if the fee authorization is missing or insufficient, the Commissioner is authorized to charge any additional fees which may be required, or credit any overpayment, to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/6257-16302/AAC.

Respectfully submitted,

Date: June 7, 2010

By: /Alex A. Courtade/

Alex A. Courtade

Reg. No. 65,635

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.  
P.O. Box 398  
Austin, TX 78767-0398  
(512) 853-8879